

## **Draft 3.0 for Review**

**For Immediate Release**

**Press Release**

### **DVCon 2011 Announces Call for Paper and Panel abstracts and Tutorial Proposals**

Louisville, CO – July 27, 2010 – The 2011 Design and Verification Conference (DVCon), sponsored by Accellera, is now accepting paper & panel abstract submissions and tutorial proposals. DVCon 2011 will be held February 28-March 3 at the DoubleTree Hotel in San Jose, California.

The premier conference for the functional design and verification of electronic systems, its focus is on the use of specialized design and verification languages. Conference attendees are primarily designers of electronic systems, ASICs and FPGAs, as well as those involved in the research, development, and application of Electronic Design Automation tools.

“We would like to see submissions for 2011 address the tremendous challenges our community is facing regarding verification closure.” stated Dr. Ambar Sarkar, DVCon 2011 Program Chair. “The 2011 conference will also bring to light the latest technological and academic advancements in design and verification. We will be drawing attention to approaches that allow verification to scale with the complexity of the design, including the adoption of community driven standards and open source offerings that are becoming increasingly popular”.

When surveyed following the 2010 conference in February, attendees said their top three reasons for attending DVCon were: Learning new methodologies to improve their verification process; Learning new techniques to improve their design process; and meeting and networking with other engineers in the industry.

Those submitting proposals are encouraged to contribute experiences with hardware design and verification languages, advanced tools and methodologies, and to participate in the valuable exchange of ideas. Suggested topics can be found at [www.dvcon.org](http://www.dvcon.org).

#### **Paper and panel proposals are due August 31, 2010**

Paper and Panel Proposals can be submitted on-line at <http://www.dvcon.org>.

#### **Sponsored Tutorial proposals are due September 16, 2010**

A limited number of sponsored special session tutorials are available. Submit all proposals on-line at <http://www.dvcon.org>.

DVCon is the premier conference for discussion of the functional design and verification of electronic systems. DVCon is sponsored by **Accellera**, an industry consortium dedicated to the development and standardization of design and verification languages. For more information about Accellera, please visit [www.accellera.org](http://www.accellera.org). For more information about DVCon, please visit [www.dvcon.org](http://www.dvcon.org). Follow @dvcon on Twitter or to comment, please use #dvcon.

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